

IRFB42N20DPbF

HEXFET® Power MOSFET

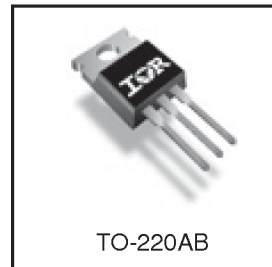
Applications

- High frequency DC-DC converters
- Motor Control
- Uninterruptible Power Supplies
- Lead-Free

V_{DSS}	$R_{DS(on) \max}$	I_D
200V	0.055Ω	44A

Benefits

- Low Gate-to-Drain Charge to Reduce Switching Losses
- Fully Characterized Capacitance Including Effective C_{OSS} to Simplify Design, (See App. Note AN1001)
- Fully Characterized Avalanche Voltage and Current



Absolute Maximum Ratings

	Parameter	Max.	Units
$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	44	A
$I_D @ T_C = 100^\circ\text{C}$	Continuous Drain Current, $V_{GS} @ 10\text{V}$	31	
I_{DM}	Pulsed Drain Current ①	180	
$P_D @ T_A = 25^\circ\text{C}$	Power Dissipation	2.4	W
$P_D @ T_C = 25^\circ\text{C}$	Power Dissipation	330	
	Linear Derating Factor	2.2	W/°C
V_{GS}	Gate-to-Source Voltage	± 30	V
dv/dt	Peak Diode Recovery dv/dt ②	2.5	V/ns
T_J	Operating Junction and	-55 to + 175	°C
T_{STG}	Storage Temperature Range		
	Soldering Temperature, for 10 seconds	300 (1.6mm from case)	
	Mounting torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

Thermal Resistance

	Parameter	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	---	0.45	°C/W
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50	---	
$R_{\theta JA}$	Junction-to-Ambient	---	62	

Notes ① through ⑤ are on page 8

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International
 Rectifier

Static @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
V _{(BR)DSS}	Drain-to-Source Breakdown Voltage	200	—	—	V	V _{GS} = 0V, I _D = 250μA
ΔV _{(BR)DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	—	0.26	—	V/°C	Reference to 25°C, I _D = 1mA
R _{DS(on)}	Static Drain-to-Source On-Resistance	—	—	0.055	Ω	V _{GS} = 10V, I _D = 26A ④
V _{GS(th)}	Gate Threshold Voltage	3.0	—	5.5	V	V _{DS} = V _{GS} , I _D = 250μA
I _{DSS}	Drain-to-Source Leakage Current	—	—	25	μA	V _{DS} = 200V, V _{GS} = 0V
		—	—	250		V _{DS} = 160V, V _{GS} = 0V, T _J = 150°C
I _{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	V _{GS} = 30V
	Gate-to-Source Reverse Leakage	—	—	-100		V _{GS} = -30V

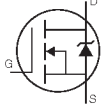
Dynamic @ T_J = 25°C (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
g _{fs}	Forward Transconductance	21	—	—	S	V _{DS} = 50V, I _D = 26A
Q _g	Total Gate Charge	—	91	140	nC	I _D = 26A V _{DS} = 160V V _{GS} = 10V,
Q _{gs}	Gate-to-Source Charge	—	24	36		
Q _{gd}	Gate-to-Drain ("Miller") Charge	—	43	65	ns	V _{DD} = 100V I _D = 26A R _G = 1.8Ω V _{GS} = 10V ④
t _{d(on)}	Turn-On Delay Time	—	18	—		
t _r	Rise Time	—	69	—		
t _{d(off)}	Turn-Off Delay Time	—	29	—		
t _f	Fall Time	—	32	—		
C _{iss}	Input Capacitance	—	3430	—	pF	V _{GS} = 0V V _{DS} = 25V f = 1.0MHz V _{GS} = 0V, V _{DS} = 1.0V, f = 1.0MHz V _{GS} = 0V, V _{DS} = 160V, f = 1.0MHz V _{GS} = 0V, V _{DS} = 0V to 160V ⑤
C _{oss}	Output Capacitance	—	530	—		
C _{rss}	Reverse Transfer Capacitance	—	100	—		
C _{oss}	Output Capacitance	—	5310	—		
C _{oss}	Output Capacitance	—	210	—		
C _{oss eff.}	Effective Output Capacitance	—	400	—		

Avalanche Characteristics

	Parameter	Typ.	Max.	Units
E _{AS}	Single Pulse Avalanche Energy②	—	510	mJ
I _{AR}	Avalanche Current①	—	26	A
E _{AR}	Repetitive Avalanche Energy①	—	33	mJ

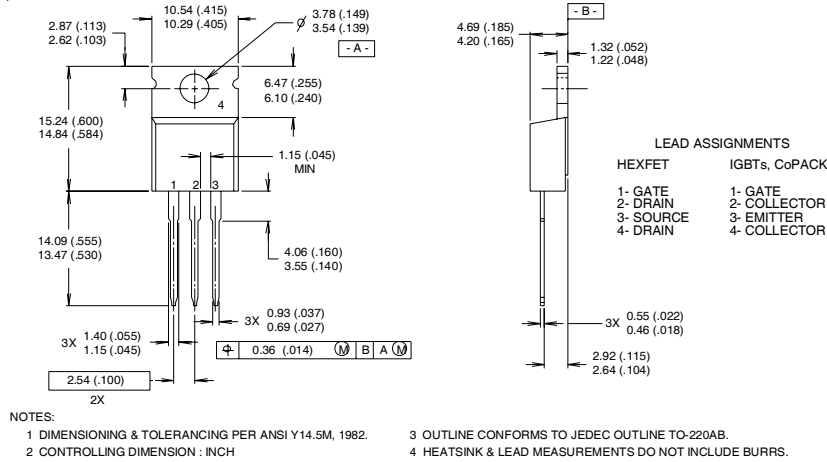
Diode Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I _S	Continuous Source Current (Body Diode)	—	—	44	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I _{SM}	Pulsed Source Current (Body Diode) ①	—	—	180		
V _{SD}	Diode Forward Voltage	—	—	1.3	V	T _J = 25°C, I _S = 26A, V _{GS} = 0V ④
t _{rr}	Reverse Recovery Time	—	220	330	ns	T _J = 25°C, I _F = 26A
Q _{rr}	Reverse Recovery Charge	—	1860	2790	nC	di/dt = 100A/μs ④
t _{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible (turn-on is dominated by L _S +L _D)				

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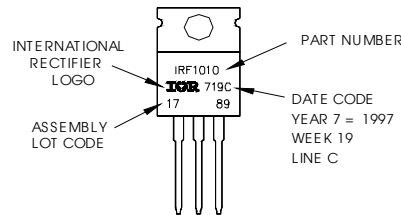


TO-220AB Package Outline



TO-220AB Part Marking Information

EXAMPLE: THIS IS AN IRF1010
 LOT CODE 1789
 ASSEMBLED ON WW 19, 1997
 IN THE ASSEMBLY LINE "C"
Note: "P" in assembly line position indicates "Lead-Free"



Notes:

- Repetitive rating; pulse width limited by max. junction temperature.
- Starting $T_J = 25^\circ\text{C}$, $L = 1.45\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = 26\text{A}$, $V_{GS} = 10\text{V}$
- $I_{SD} \leq 26\text{A}$, $di/dt \leq 110\text{A}/\mu\text{s}$, $V_{DD} \leq V_{(BR)DSS}$,
 $T_J \leq 175^\circ\text{C}$
- Pulse width $\leq 400\mu\text{s}$; duty cycle $\leq 2\%$.
- C_{OSS} eff. is a fixed capacitance that gives the same charging time as C_{OSS} while V_{DS} is rising from 0 to 80% V_{DSS}

Data and specifications subject to change without notice.
 This product has been designed and qualified for the Industrial market.

